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Harold C. Moore

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Signature

February 18, 2003

Date of Signature

Re: Application of: Newell E. Chiesl
 Serial No.: 09/960,441
 Filed: September 21, 2001
 For: Arrangement for Measuring Pressure on a
 Semiconductor Wafer and an Associated
 Method for Fabricating a Semiconductor
 Wafer
 Group Art Unit: 2812
 Examiner: Viktor Simkovic
 Our Docket No.: 1003-0610

TRANSMITTAL OF RESPONSE TO OFFICE ACTION

Please find for filing in connection with the above patent application the
following:

1. Response to Office Action.

DOCKETED
2/19/03 lam

* The fee has been calculated as shown below.

CLAIMS AS AMENDED				
	Claims Remaining After Amendment	Highest No. Paid For	Fee Calculation	Addit Fee
Total Claims	11	20	0 X 9	\$ 0.00
Independent Claims	2	3	0 X 42	\$ 0.00
Total Additional Fee Required				\$ 0.00

Please charge any fee deficiency, or credit any overpayment, to Deposit Account No. 13-0014; but not to include any payment of issue fees.

Respectfully Submitted,

MAGINOT, MOORE & BOWMAN



February 18, 2003

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Enclosures


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3	4	03	6					
3	4	03	7					
3	12	03	88					
3	13	03	356					
3	14	03	195					
3	25	03	1					
3	28	03	19					
				Twel20 <09960441>	1003-0610 01-384	1201	84.00	2942.00
				09960441	01-384	1202	126.00	2816.00
				10347850	1008-0538	9204	-375.00	3191.00
				PCT/US02/24787	PCT 16710237	170566121	146.00	3045.00
				PCT/US02/24788	PCT 1671-0238	170566122	146.00	2899.00
				09941205	1570-0018	181466123	110.00	2789.00
				5937971	1568-0002	255166124	5.00	2784.00
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				3026.00	617.00	375.00	2784.00	

*** O.D. INDICATES OVERDRAWN